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JL		ASHOK K. SHARMA, "Programmable Logic Handbook – PLDs, CPLDs, & FPGAs", 1998, pp 99-171, McGraw-Hill, USA	<input type="checkbox"/>
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Examiner Signature	James Cho	Date Considered	7/20/05
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¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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